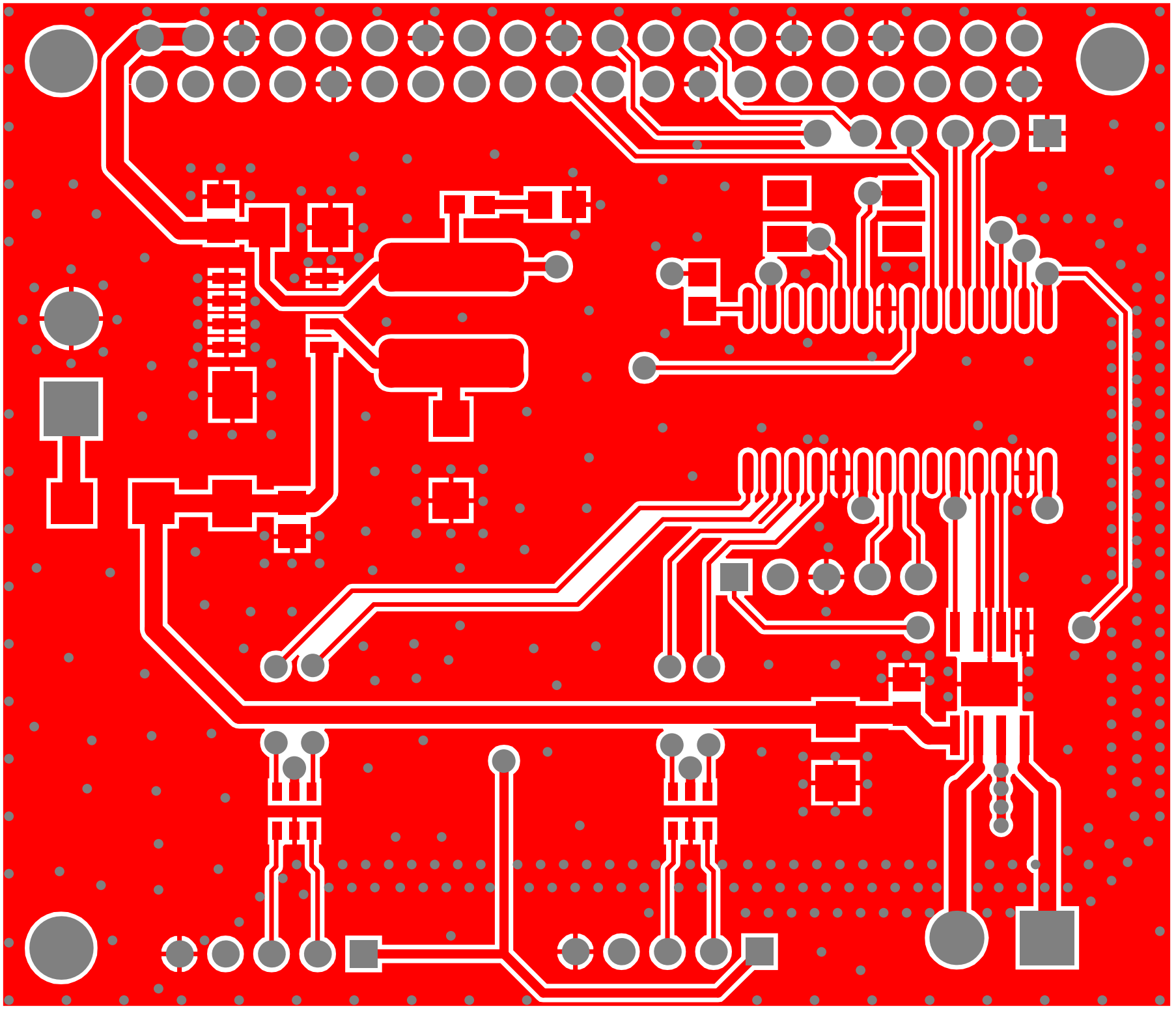
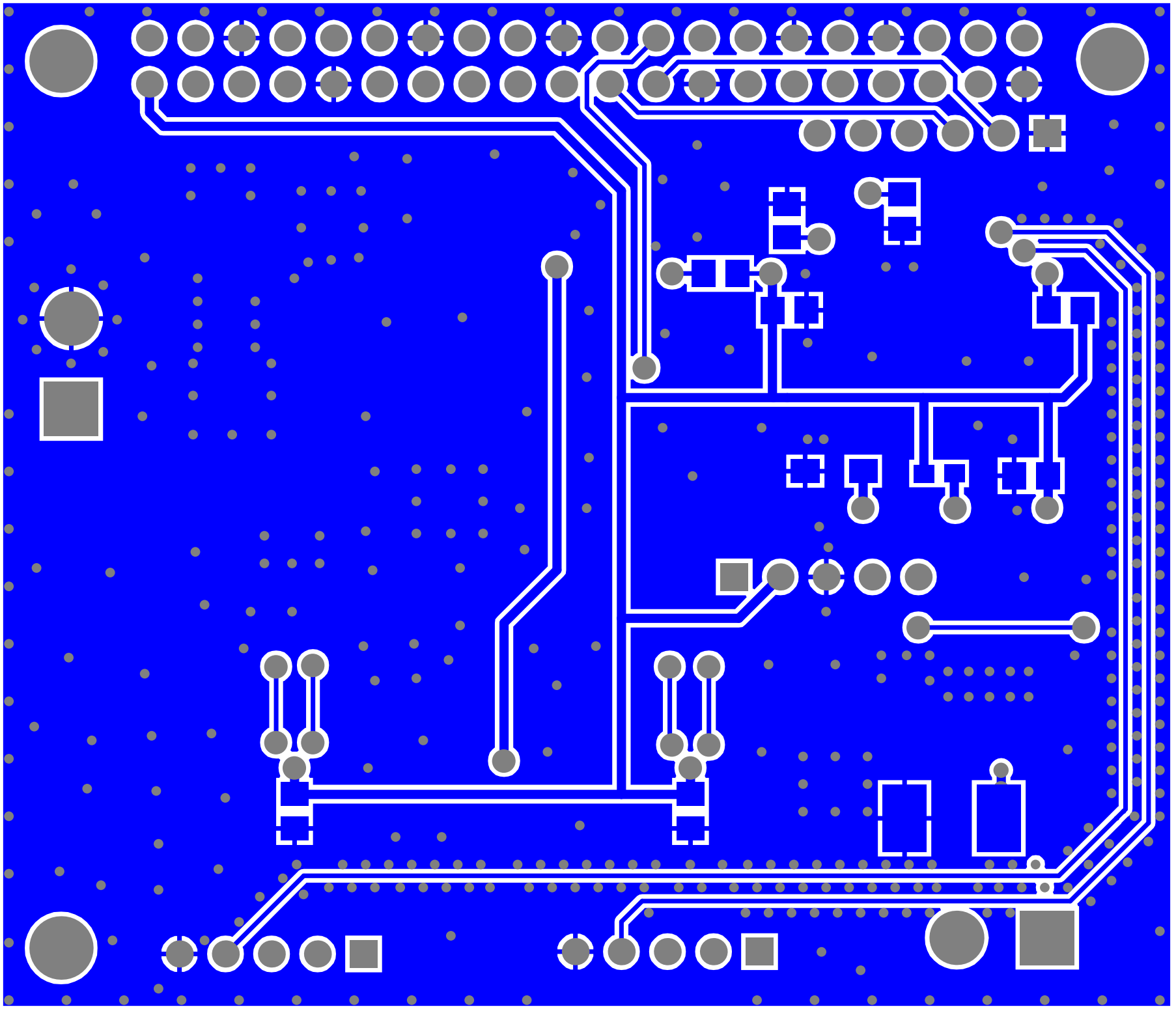
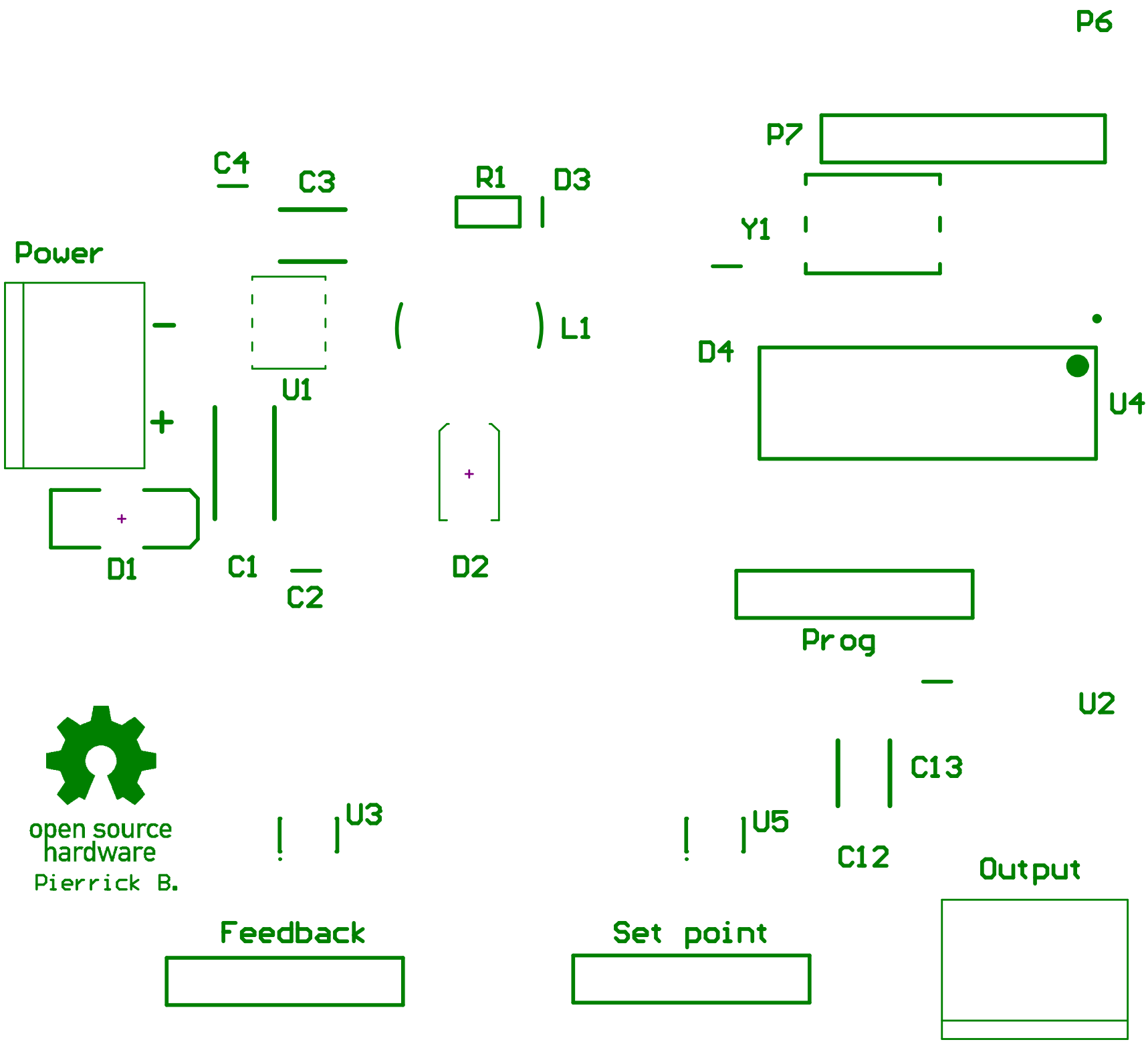
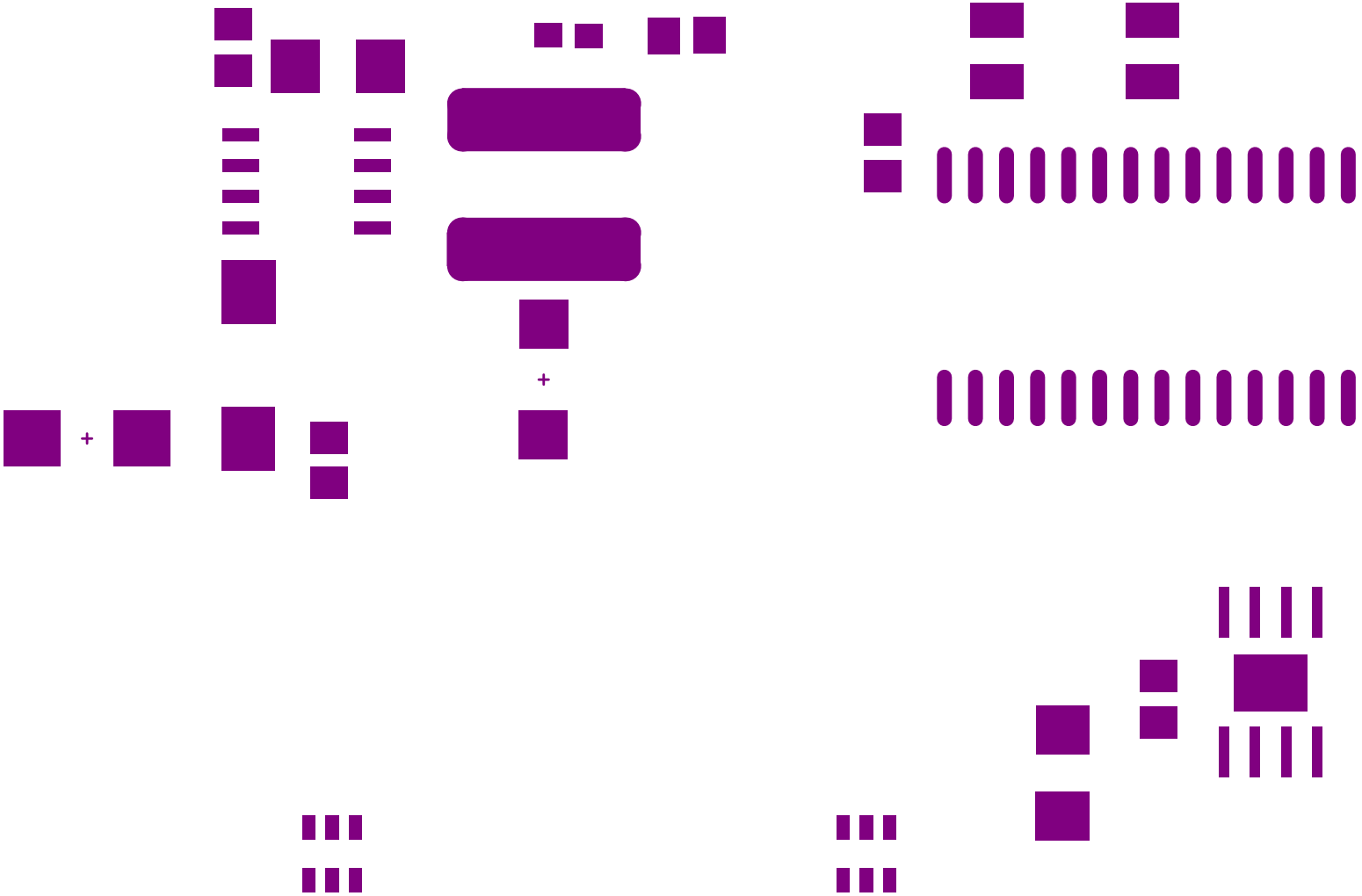
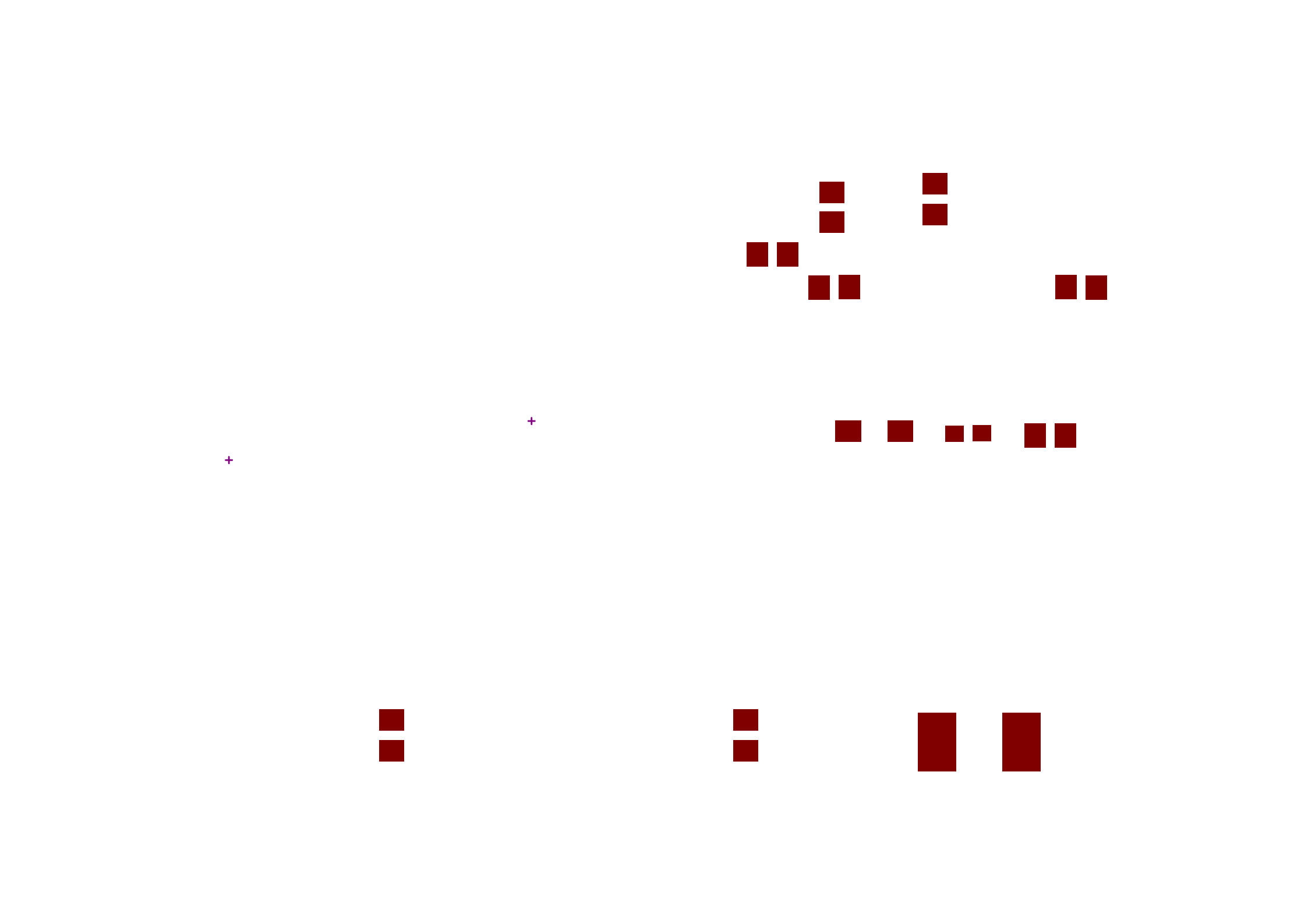
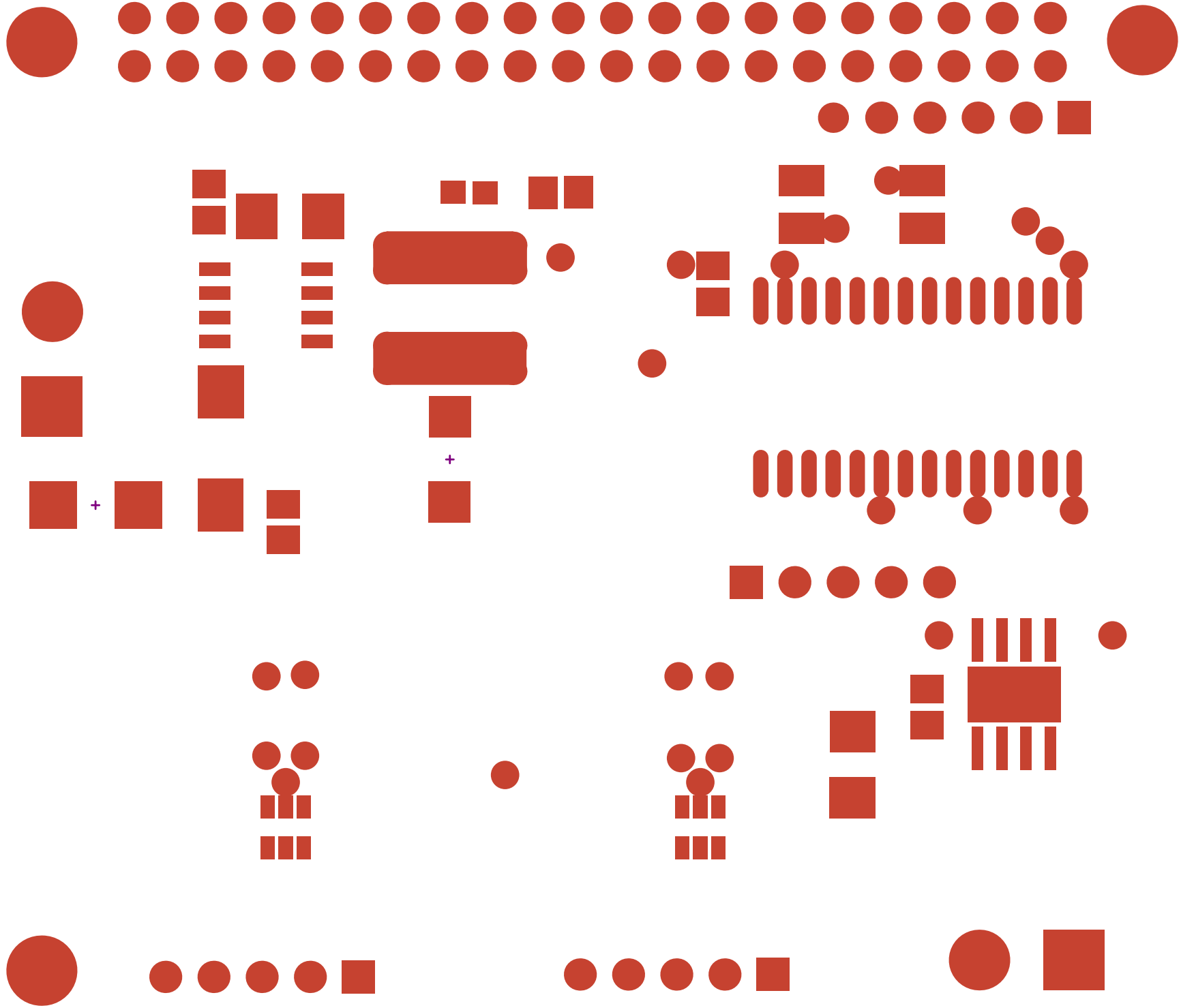
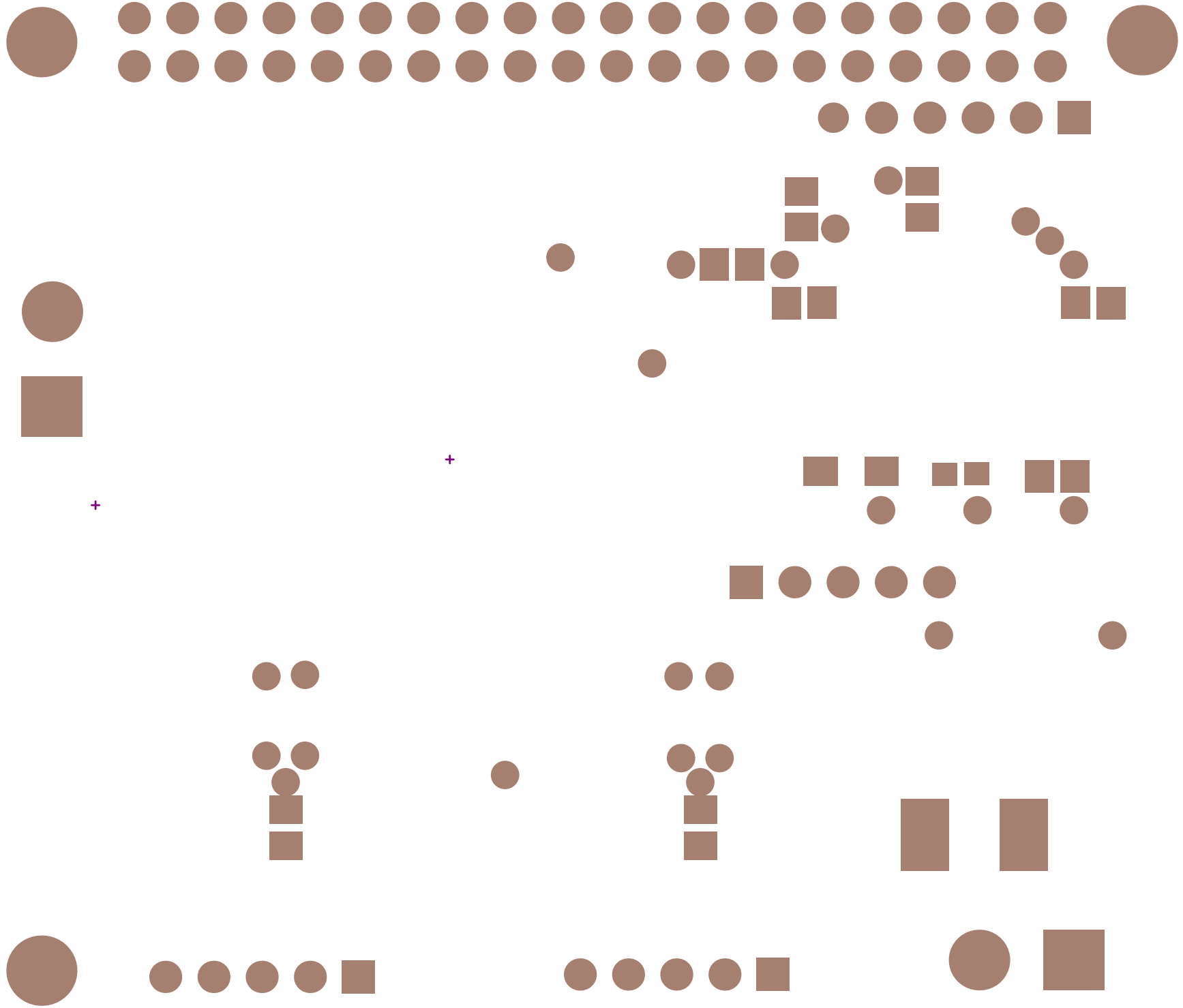
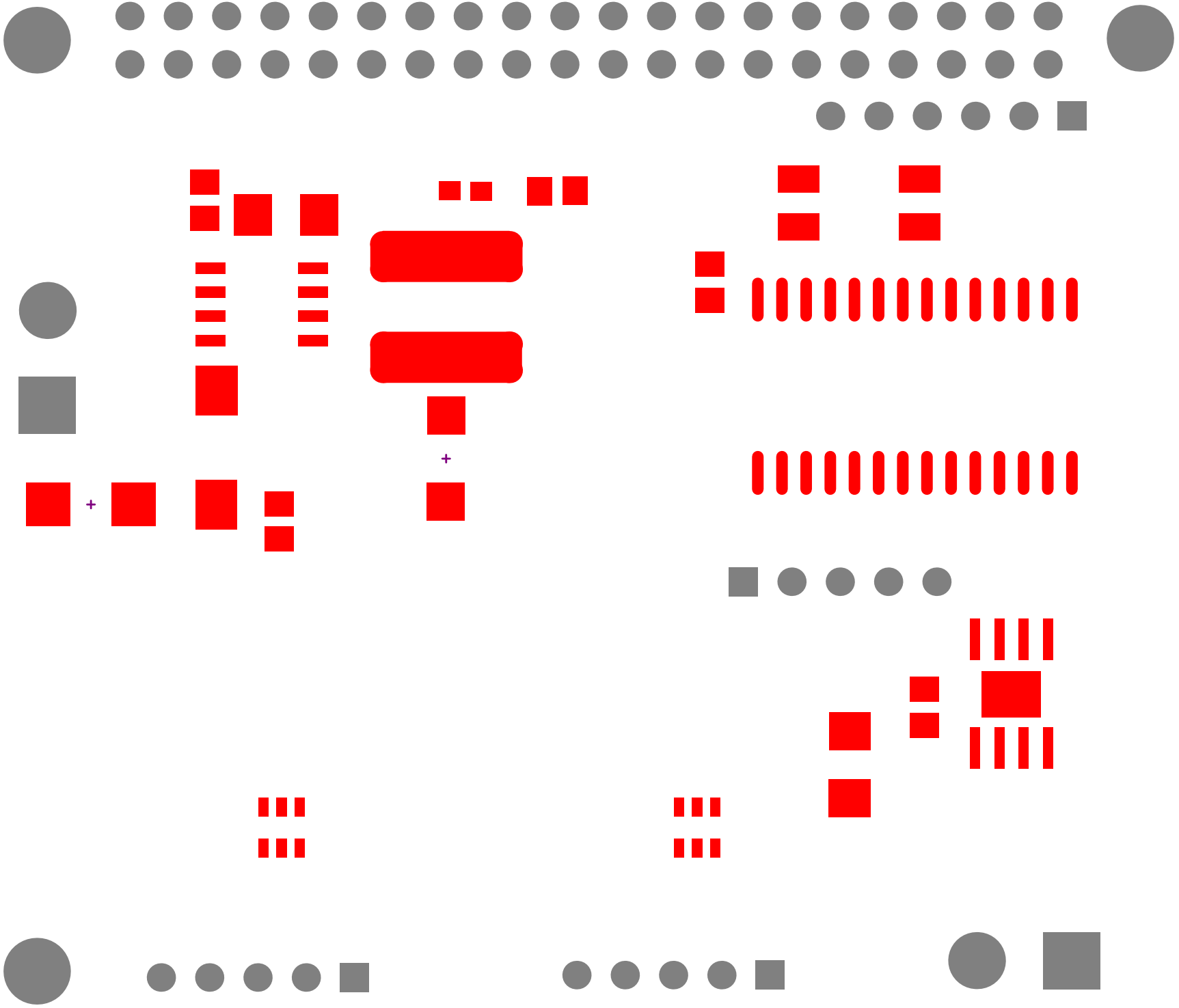
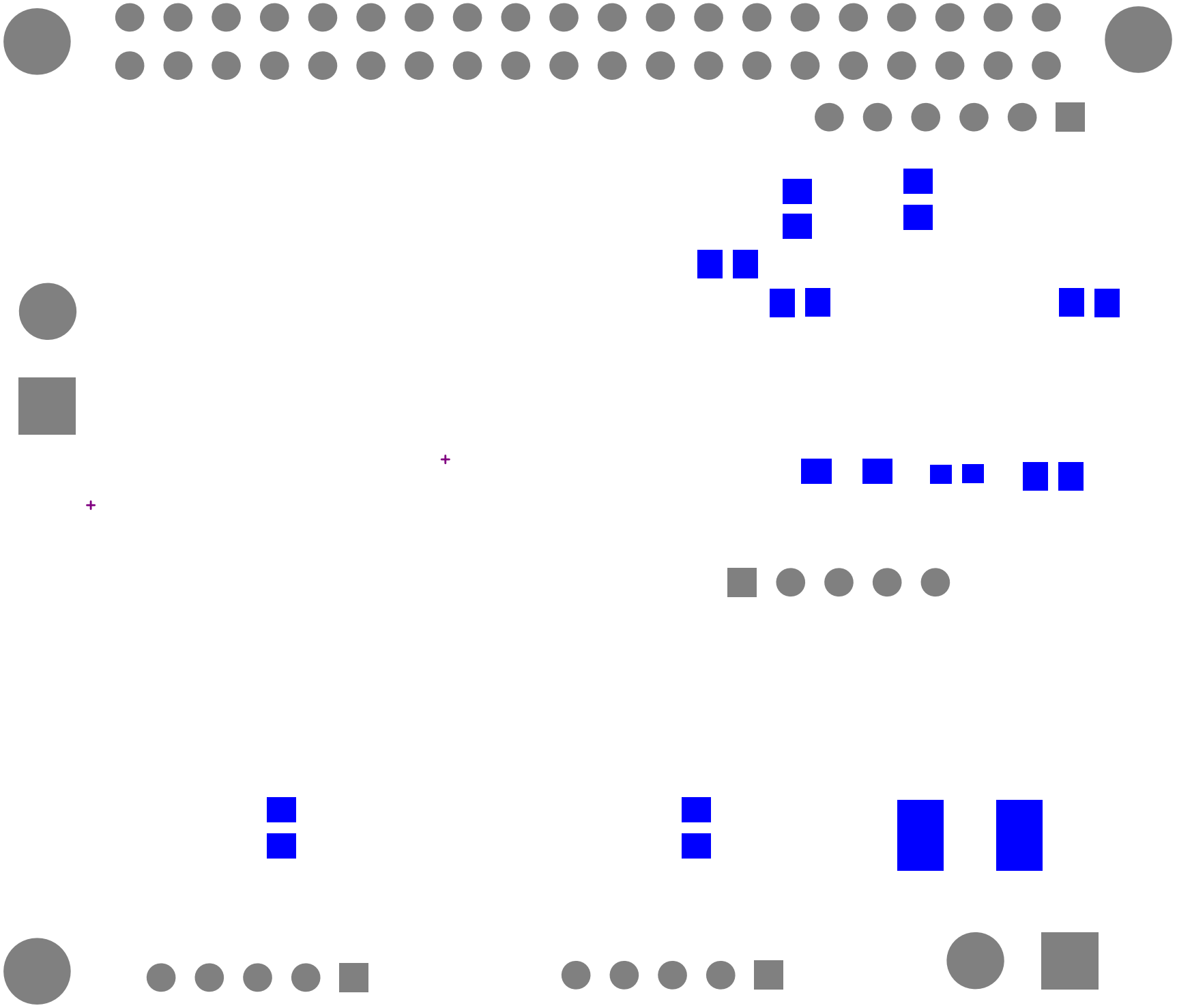
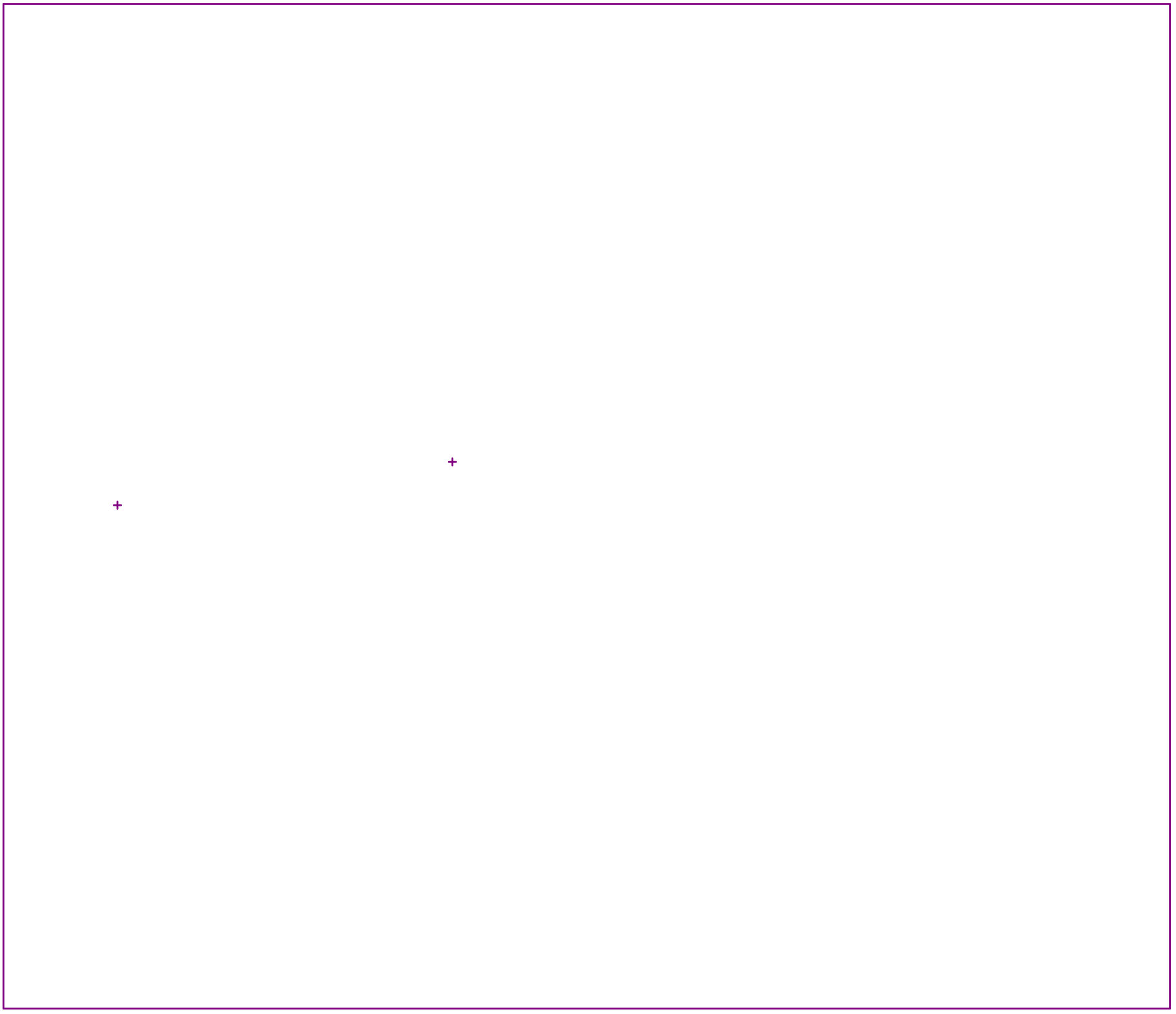
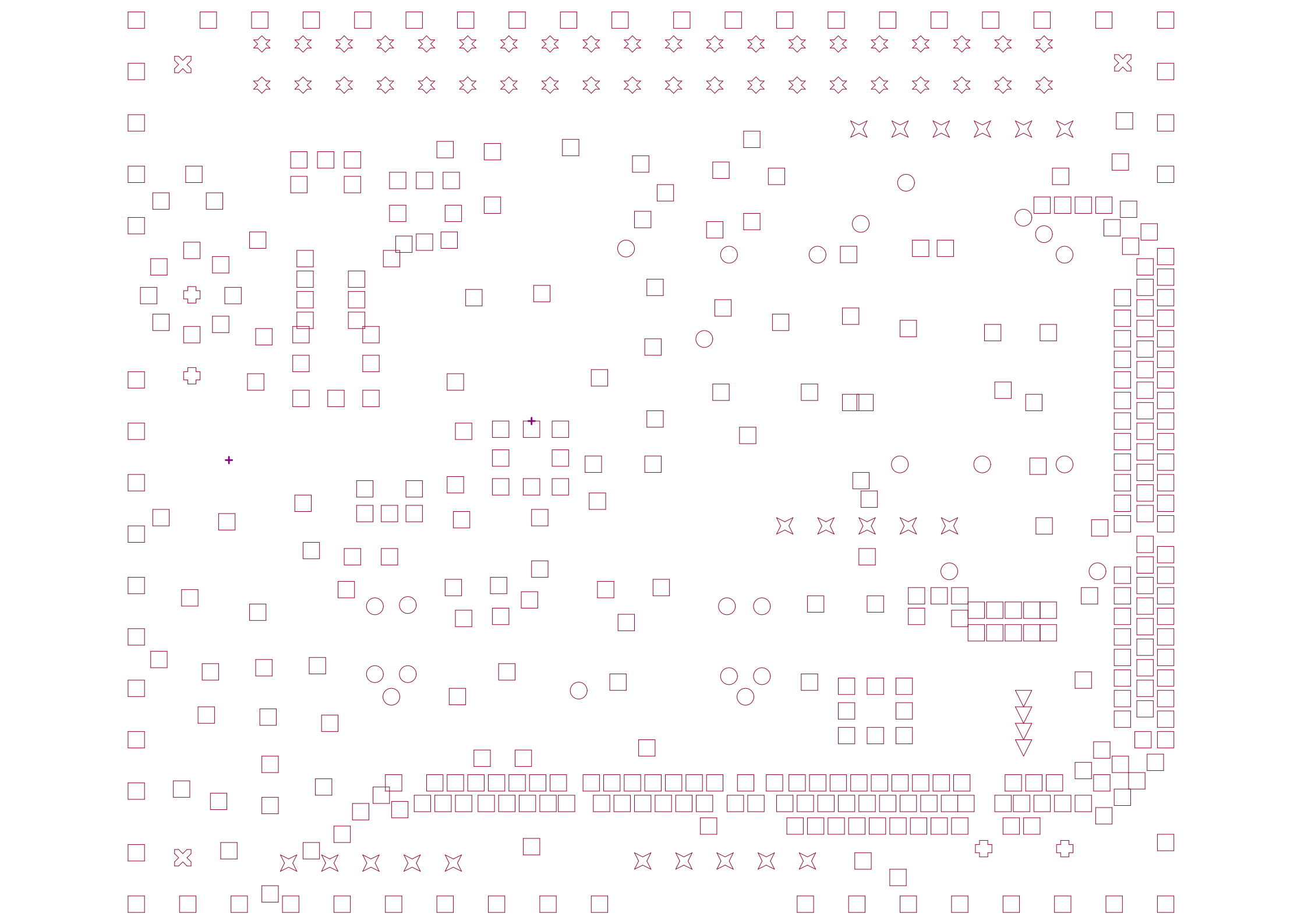
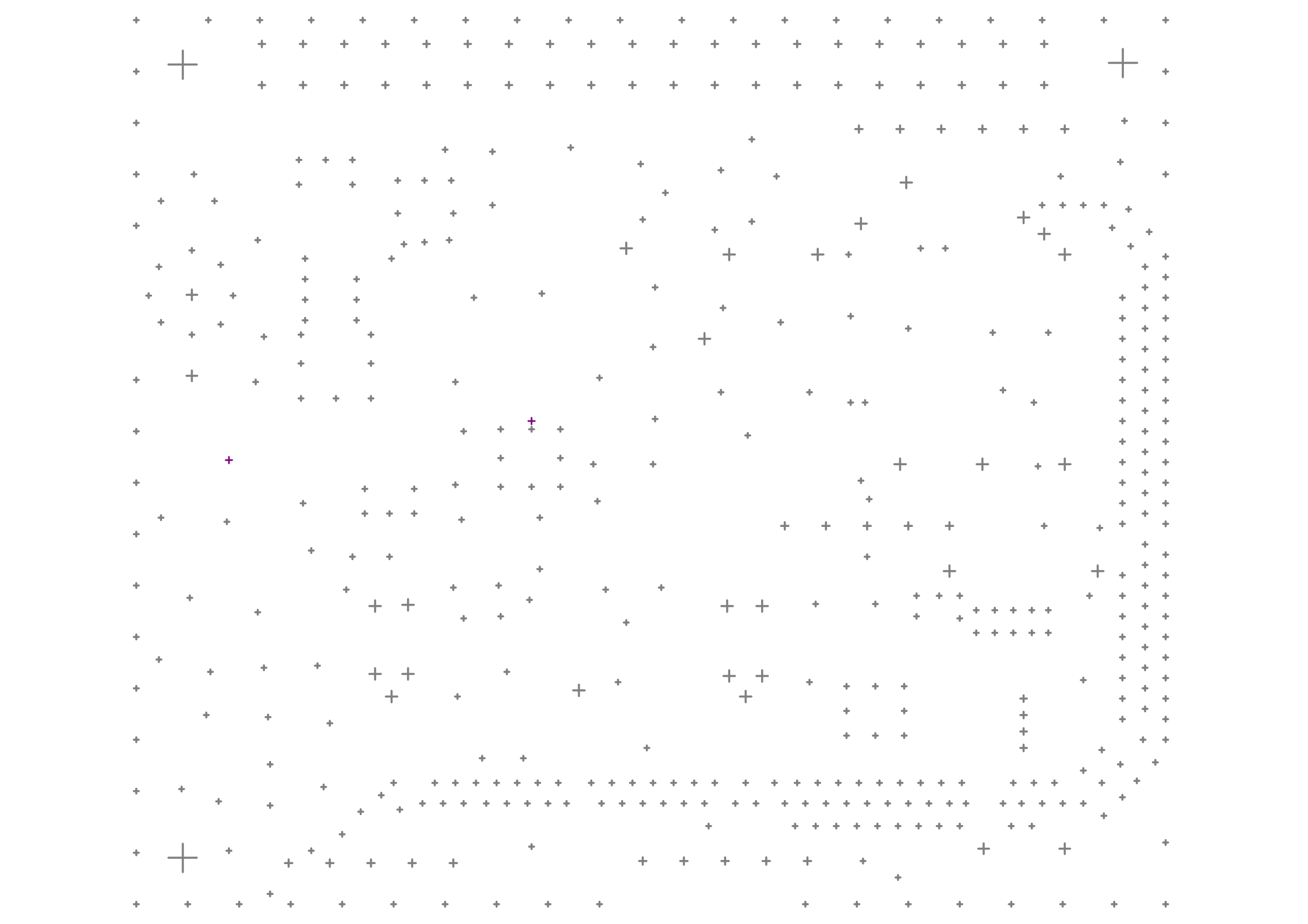





P6
P7
C4
C3
R1
D3
Y1
Power
L1
U1
D4
U4
D1
C1
C2
D2
Prog
U2
C13
U3
U5
C12
Output
open source
hardware
Pierrick B.
Feedback
Set point

C7
C6
R3
C10
R4
C11
R5
C9
R2
C8
C5